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A Publication of the Materials Research Society

June 1993, Volume XVIII, No. 6



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June 1993

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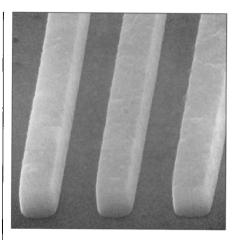
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**ON THE COVER**: Copper lines made by selective electroless copper deposition on Au/Ti seeding inside open trenches in PMMA matrix. The Cornell Nanoline Copper Technology was developed by Y. Shacham-Diamand at Cornell University under SRC funding. The electronbeam exposure was done by R. Tiberio at the National Nanofabrication Facility (NNF) at Cornell University. For more information on this topic, see "Electroless Cu for VLSI," by J.S.H. Cho, H-K. Kang, S.S. Wong, and Y. Shacham-Diamand, p. 31.

# S BULLE

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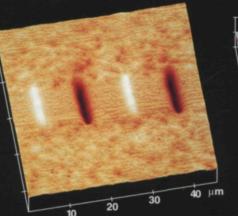
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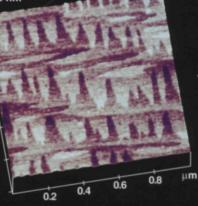
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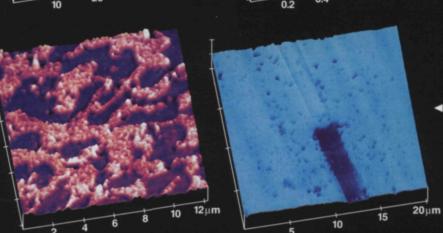


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These 1.6Å-high terraces of epitaxially-grown silicon were imaged using the NanoScope Large Sample Stage. Only the AFM probe touched the top surface of the intact 8in wafer.

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4.0 nm

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